

ABSTRACT

A circuit board having coupled multilayered ceramic capacitors mounted thereon considerably reduces the generation of sounds developed by piezoelectric effects in the capacitors. A method for mounting the capacitors on the circuit board includes the step of forming lands for mounting the capacitors thereon at substantially plane-symmetrical positions on the front surface and the back surface of the circuit board, two lands at their substantially plane-symmetrical positions being connected each other. The capacitors, which are substantially identical each other, are then mounted on the lands of the front and the back surfaces such that the capacitors are disposed at substantially plane-symmetrical positions and electrically coupled to the lands. As a result, vibrations being transferred from the capacitors to the circuit board are cancelled out with each other and the resonance of the circuit board due to the vibrations from the capacitors is prevented, resulting in the generation of audible sounds with high sound pressure levels being considerably reduced compared to that of the prior art.